

10/512086

DT15 Rec'd PCT/PTO 08 NOV 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#4/  
A

Sub-Spec(NE)

In re the application of:

**Georg Rudolf SILLNER**

Group Art Unit:

Serial No:

Examiner:

Filed :

For : **METHOD FOR PROCESSING ELECTRICAL  
PARTS, PARTICULARLY FOR PROCESSING  
SEMICONDUCTOR CHIPS AND ELECTRICAL  
COMPONENTS, AND DEVICE FOR CARRYING  
OUT SAID METHOD**

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Prior to examination on the merits, please amend the  
application as follows: